Docket No.

251148US0/phh

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Yul

Yuko UCHIMARU, et al.

SERIAL NO:

10/809,704

GAU:

FILED:

March 26, 2004

EXAMINER:

FOR:

LOW DIELECTRIC CONSTANT INSULATING MATERIAL AND SEMICONDUCTOR DEVICE USING THE

MATERIAL

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. In accordance with discussions on August 4, 2004 with Mr. Nicholas P. Godici, Commissioner for Patents, it is no longer required to submit copies of cited pending applications. A modification of the Rules will be published soon in the Official Gazette. Cited issued patents, if any, are listed on the attached PTO form 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number <u>15-0030</u>. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Norman F. Oblon

Customer Number

22850

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

IN THE UNITED STATES PASPENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Yuko UCHIMARU, et al.

SERIAL NO: 10/809,704

GROUP:

FILED:

March 26, 2004

EXAMINER:

FOR:

LOW DIELECTRIC CONSTANT INSULATING MATERIAL AND

SEMICONDUCTOR DEVICE USING THE MATERIAL

STATEMENT OF RELEVANCY

All of the references on Form PTO-1449 are discussed in the specification.

NOV 1 5 2004 W SHEET 1 OF 1

Form PTO 1449		U.S. DEPARTMENT (ATTY DOCKET NO SERIAL NO.			
(Modified)		PATENT AND TRADI	EMARK OFFICE	251148US0 RADEWE		10/809,704	
				APPLICANT			
LIST OF REFERENCES CITED BY APPLICANT				Yuko UCHIMARU, et al.			
				FILING DATE		GROUP	
				March 26, 2004			
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
1	AG						
	AH						
	Al						
	AJ					,	
	AK						
	AL						
	АМ						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT	DATE	COUNTRY		TRANSLATION	
		NUMBER				YES	
	AO	2002-317049	10/31/2002	JAPAN (with English Abstract)			X
	AP	2968244	08/20/1999	JAPAN (with English Abstract)			X
	AQ	3041424	03/10/2000	JAPAN (with translation of claim 1)			X
	AR	2002-359240	12/13/2002	JAPAN (with English Abstract)			X
	AS	2003-119289	04/23/2003	JAPAN (with English Abstract)	-		X
	AT						
	AU					ļ	
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
	AW International Technology Roadmap for Semiconductors 2002 Update, 2002 Update Tables, page 75, "TABLE 62A MPU INTERCONNECT TECHNOLOGY REQUIREMENTS—NEAR-TERM", 2002						
	AX	S. SHINMIYAHARA, et al., Cu-Interconnect Technology, page 227, "LATEST DEVELOPMENT OF CU WIRING TECHNIQUES", (with partial English translation)					
	AY						
	AZ				Add	itional Refe	erences sheet(s) attached
Examiner					Date Considered		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							